

Interference Searched
McK 6/29/07

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L24	5087	(711/167,154,220).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/29 12:05
L25	0	L24 AND (command\$1 AND address\$2 AND signal\$1 AND active AND bank ADJ address ADJ signal\$1 AND pin\$1 AND chip ADJ select AND (row ADJ address ADJ strobe OR RAS) AND (column ADJ address ADJ strobe OR CAS) AND write ADJ enable).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:08

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3023	(command\$1 near5 address\$2) near5 (CLK\$1 OR clock OR edge\$1 OR transition\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:47
L2	7703	SLDRAM\$1 OR RDRAM\$1 OR (XDR ADJ DRAM\$1) OR DRDRAM\$1 OR D-RDRAM\$1 OR Ramlink\$1 OR synclink\$1 OR ((rambus\$1 OR synclink\$1 OR (synchronous ADJ link)) ADJ DRAM\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:47
L3	10	L2 near5 L1	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:53
L4	71	((command near4 address) near5 (signal\$1 OR bit\$1)) WITH ((bank ADJ address\$3 OR BA) near5 (row ADJ address\$3 OR RA OR RAS))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:54
L5	21	((command near4 address) near5 (signal\$1 OR bit\$1)) WITH ((bank ADJ address\$3 OR BA) near5 (row ADJ address\$3 OR RA OR RAS) near5 (column ADJ address\$3 OR CA OR CAS))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:54
L6	2	(A8 OR RA8) near5 ((RAS OR RA) near5 pin\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:54
L7	58	((row ADJ address near3 (bit\$1 OR signal\$1)) OR A8 OR RA8) near5 (RAS near5 pin\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:55
L8	3	((row ADJ address) near3 (data OR bit\$1 OR signal\$1)) near5 (shar\$3 OR us\$3) near5 (select\$3 near3 pin\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:55
L9	9	((row ADJ address) near5 (data OR bit\$1 OR signal\$1)) WITH (shar\$3 OR us\$3) WITH (control\$4 near5 pin\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:58
L10	11	((row ADJ address) near5 (data OR bit\$1 OR signal\$1)) WITH (shar\$3 OR us\$3) WITH (select\$3 near5 pin\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:59

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L11	10	((row ADJ address near5 (bit\$1 OR signal\$1)) OR A7 OR RA7) WITH ((BA0 OR bank ADJ address) near5 pin\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 11:59
L12	53	((address) near2 (data OR bit\$1 OR signal\$1)) near5 (shar\$3 OR us\$3) near5 ((command OR control) near2 pin\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:00
L13	3023	(command\$1 near5 address\$2) near5 (CLK\$1 OR clock OR edge\$1 OR transition\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:00
L14	7703	SLDRAM\$1 OR RDRAM\$1 OR (XDR ADJ DRAM\$1) OR DRDRAM\$1 OR D-RDRAM\$1 OR Ramlink\$1 OR synclink\$1 OR ((rambus\$1 OR synclink\$1 OR (synchronous ADJ link)) ADJ DRAM\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:00
L15	54	L14 SAME L13	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:03
L16	5203	(711/167-169,154).CCLS.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2007/06/29 12:03
L17	1770	(711/104-105).CCLS.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2007/06/29 12:03
L18	6956	(365/233,194,189.02,230.02,230.08).CCLS.	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	OFF	2007/06/29 12:03
L19	3023	(command\$1 near5 address\$2) near5 (CCLK\$1 OR CLK\$1 OR clock OR edge\$1 OR transition\$1)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:03
L20	6654	(address\$2) near5 ((second OR next OR subsequent\$2 OR follows OR following) near5 (CCLK\$1 OR CLK\$1 OR clock OR edge\$1 OR transition\$1))	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:03
L21	222	L19 near5 L20	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:03

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L22	80	L21 AND (L16 OR L17 OR L18)	US-PGPUB; USPAT; DERWENT; IBM_TDB	OR	ON	2007/06/29 12:03
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